

Name of Course: PCB TESTING, DEBUGGING AND TROUBLESHOOTING. Duration: 30 hours

Sr.no	Contents	Hrs.
1	 PCB debugging: Introduction. Testing tools. Component description and value. Methods of Testing. 	10
2	PCB testing: • Cold testing. • Supply testing. • Soldering • Desoldering • Final testing	20



Report

Duration:

Venue: VIVA Institute Of Technology, Shirgaon.

Electronics & Telecommunication Engineering Department of VIVA Institute of Technology organized a 30 hours' certificate course on "PCB TESTING, DEBUGGING AND TROUBL ESHOOTING." during 8th January 2024 to 2nd May 2024.

The workshop was basically aimed to provide basic knowledge to second year of semester 4– EXTC students of PCB testing and debugging which was conducted by Prof.Kushal Suvarna a s the speaker who guided the participants regarding how troubleshooting of PCB is done and t he PCB is repaired.

Students were taught how to use multimeter and do testing using multimeter and how track is repaired and how components are replaced.

The students gave positive feedback for the bridge course and the curriculum gap was filled regarding PCB troubleshooting.



Course Objectives

- 1. To understand the basic fundamentals of using multimeter for testing
- 2. To understand the basic fundamentals of pcb debugging.
- 3. To understand the basic fundamentals of fault removal.

Course Outcomes:

After successful completion of the course, the students are able to

- 1. To know the basics of multimeter.
- 2. To know the basics of pcb debugging and fault finding.
- 3. To test faults in pcb by cold testing and to know the fundamentals of supply testing.



CO-PO MAPPING

Course	Program Outcome												
Outcome	PO 1	PO 2	PO 3	PO 4	P0 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	CO Target level
CO1	3	3	3	-	-	-	-	-	-	-	-	-	3
CO2	3	3	3	-	-	-	-	-	-	-	-	-	3
CO3	3	3	3	-	-	-	-	-	-	-	-	-	3
	3	3	3	-	-	-	-	-	-	-	-	-	3



Model Certificate





STUDENT CERTIFICATE

	VIVA Institute of Technology Shirgaon, Virar (East), Dist: Palghar-401305, Maharashtra
6000	Website: www.viva-technology.org
	NAAC "B++" Grade
(Carti	ificate of Annrocistian
U EIII	illiale of Appleliation
This to certify that Mr. / Mr.	DRA JADATI MANISH RHANIT CHAINMATI
student of Electronics & Telecommun	nication Department has successfully completed bridge course on
SUGERIO ELECTRONICS & TELECOMMUN	TRANSPORTED TRANSPORTATION COMPLETE A TRANSPORT
PCB IESTIMUERUSERING AN	AD IROUBLESHOOTING at VIVA institute of fechnology, shings
Virar(E) conducted between 9 ^m January	y 2024 to 2 ^{au} April 2024. His / Her contribution is appreciated.
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